



AGH UNIVERSITY OF SCIENCE AND TECHNOLOGY  
IM. STANISŁAWA STASZICA W KRAKOWIE  
Faculty of Computer Science, Electronics and Telecommunications  
DEPARTMENT OF ELECTRONICS



# ELECTRONIC DEVICES

**Piotr Dziurdzia, Ph.D.**

C-3, room 413; tel. 617-27-02, [Piotr.Dziurdzia@agh.edu.pl](mailto:Piotr.Dziurdzia@agh.edu.pl)



# BIPOLAR JUNCTION TRANSISTOR



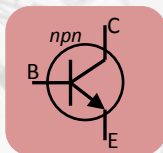
# BIPOLAR JUNCTION TRANSISTOR

## INTRODUCTION

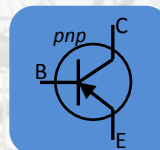


*Do you know that .....*

*.... until recently, bipolar transistor has been the most widely used semiconductor device. By saying the word "transistor" it was meant a bipolar transistor.*



*.... the current flowing between the two terminals of the bipolar transistor is controlled by a relatively small current flowing through the third terminal.*



*.... in bipolar transistor, both electrons and holes are involved in current flow.*

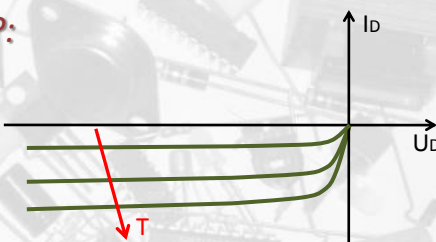
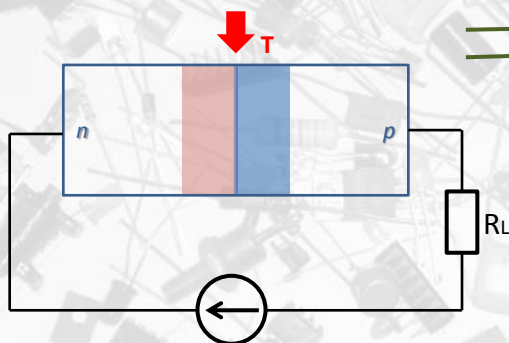


# BIPOLAR JUNCTION TRANSISTOR

## INTRODUCTION



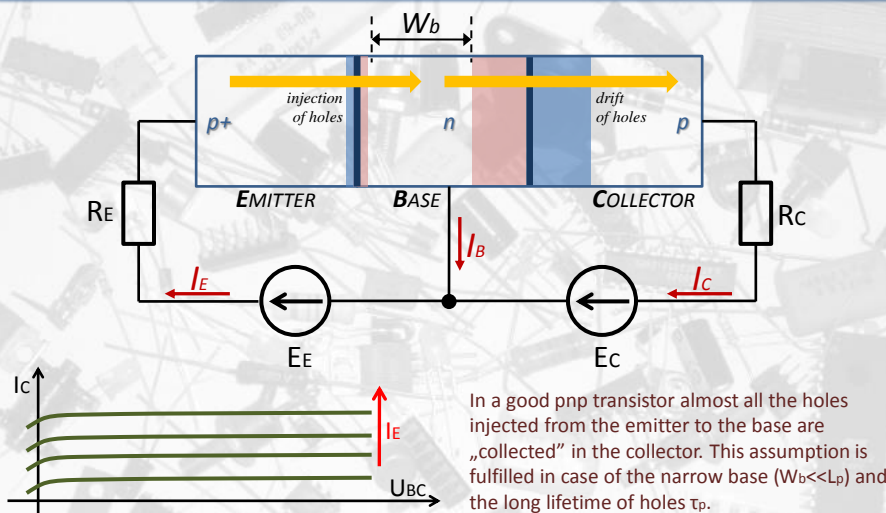
*How was it with a diode .....*



*Other ways of increasing the current .....*

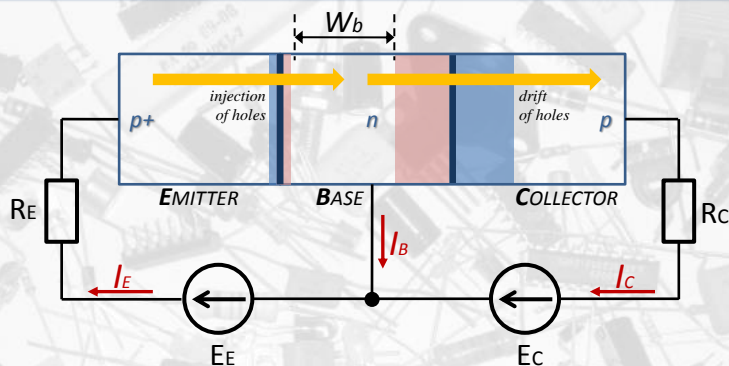
# BIPOLAR JUNCTION TRANSISTOR

## INTRODUCTION




# BIPOLAR JUNCTION TRANSISTOR

## INTRODUCTION




The base current flow consist of:

1. Current of electrons recombining with holes in the base.
2. Current of electrons injected into the emitter despite the emitter is heavily doped than the base.
3. A small current of electrons (resulting from thermal generation) flowing into the base from the reverse polarized collector junction.

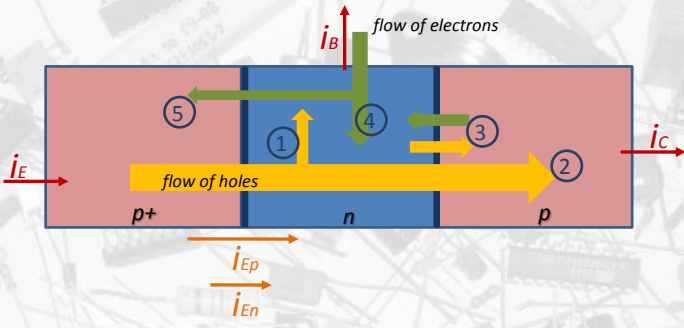


# BIPOLAR JUNCTION TRANSISTOR

## BALANCE OF ELECTRONS AND HOLES FLOW




---



- ① injected holes that are lost during recombination in the base
- ② holes reaching the reverse biased collector junction
- ③ thermal generation of electrons and holes forming the saturation current of the reverse biased collector
- ④ electrons supplied by the base contact and recombining with holes
- ⑤ electrons injected into the emitter through the junction


from: „Przyrządy półprzewodnikowe”, Ben G. Streetman

EIT PD
Electronic devices – Bipolar Junction Transistor
7

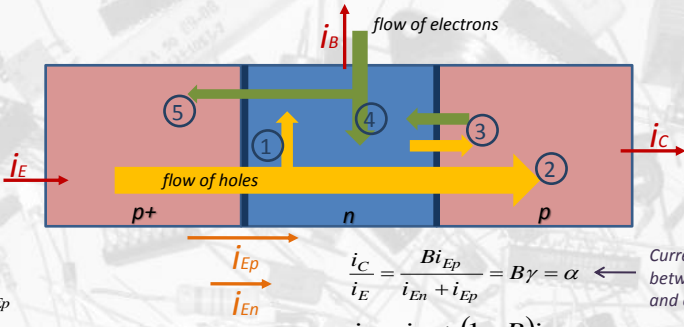


# BIPOLAR JUNCTION TRANSISTOR

## CURRENT GAIN



---



$i_C = \beta i_{E_p}$

↑  
Base transport factor (what part of the injected holes reached the collector via the base)

$\gamma = \frac{i_{E_p}}{i_{E_n} + i_{E_p}}$

↑  
The emitter injection efficiency

$\frac{i_C}{i_E} = \frac{\beta i_{E_p}}{i_{E_n} + i_{E_p}} = B\gamma = \alpha$  ← Current gain between collector and emitter

$i_B = i_{E_n} + (1 - B)i_{E_p}$

$\frac{i_C}{i_B} = \frac{\beta i_{E_p}}{i_{E_n} + (1 - B)i_{E_p}} = \frac{B[i_{E_p} / (i_{E_n} + i_{E_p})]}{1 - B(i_{E_p} / (i_{E_n} + i_{E_p}))}$

$\frac{i_C}{i_B} = \frac{B\gamma}{1 - B\gamma} = \frac{\alpha}{1 - \alpha} = \beta = \frac{\tau_p}{\tau_n}$

from: „Przyrządy półprzewodnikowe”, Ben G. Streetman

EIT PD
Electronic devices – Bipolar Junction Transistor
8

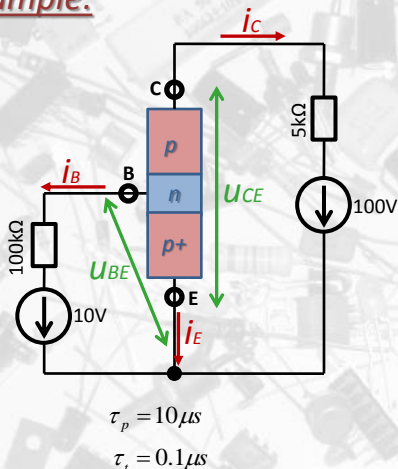


# BIPOLAR JUNCTION TRANSISTOR



## COMMON EMITTER AMPLIFIER - QUALITATIVE DESCRIPTION

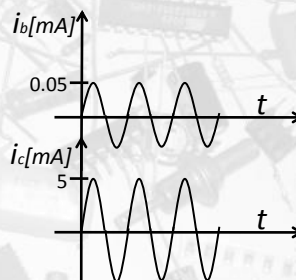
**Example:**



$$\frac{i_C}{i_B} = \beta = \frac{\tau_p}{\tau_t} = 100$$

$$I_B = \frac{10V}{100k\Omega} = 0.1mA$$

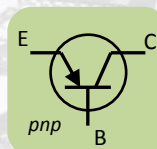
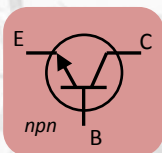
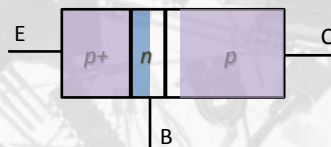
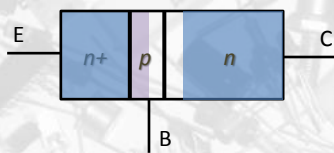
$$I_C = \beta I_B = 10mA$$



# BIPOLAR TRANSISTOR



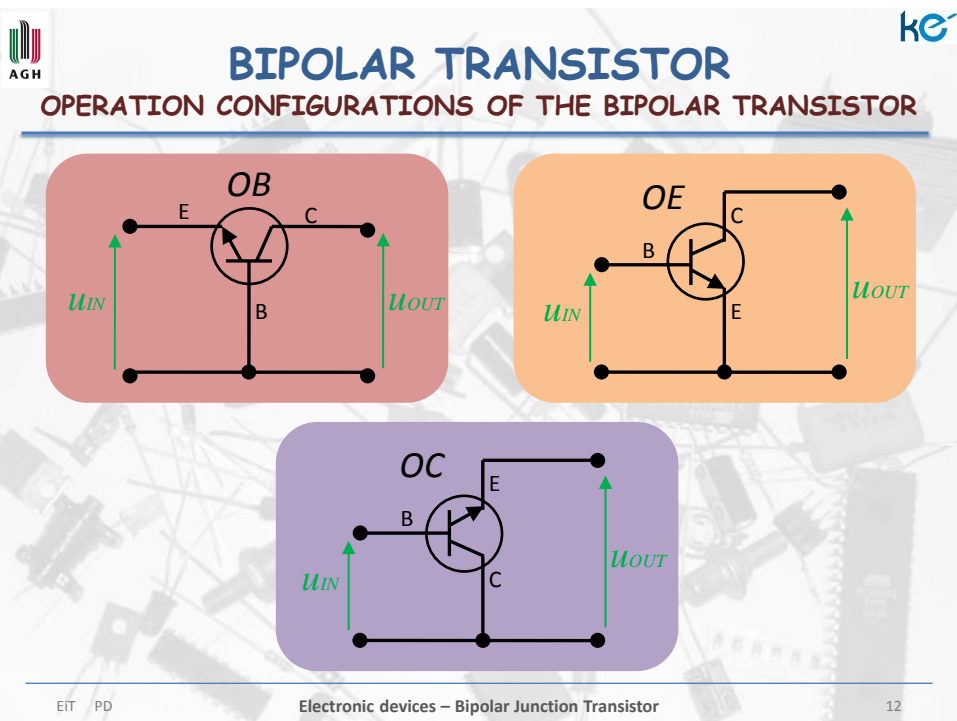
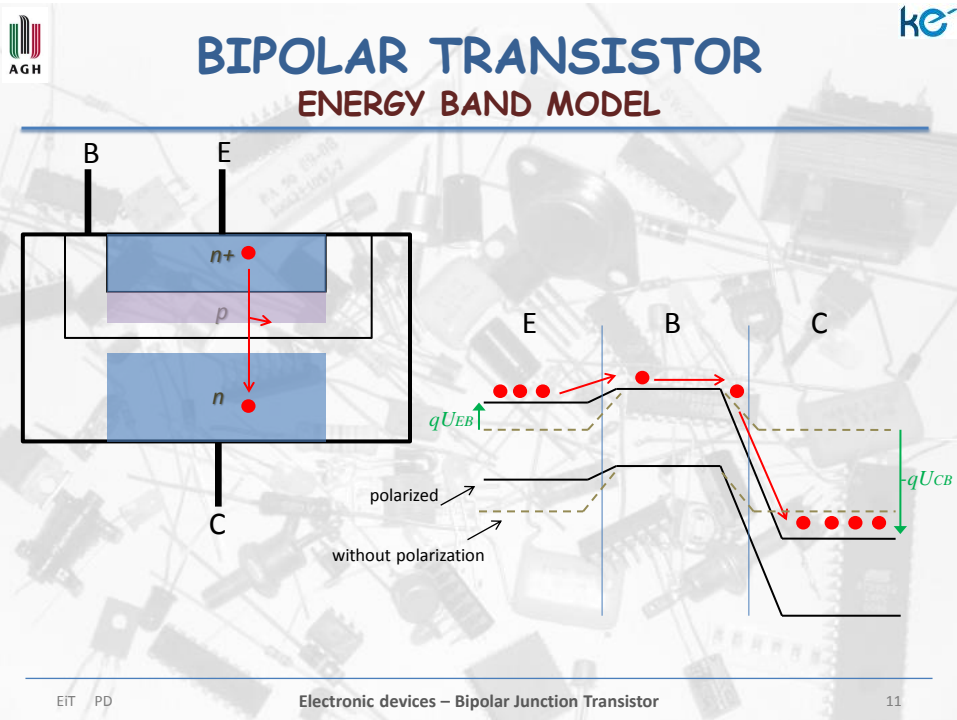
## STRUCTURES



E - emitter

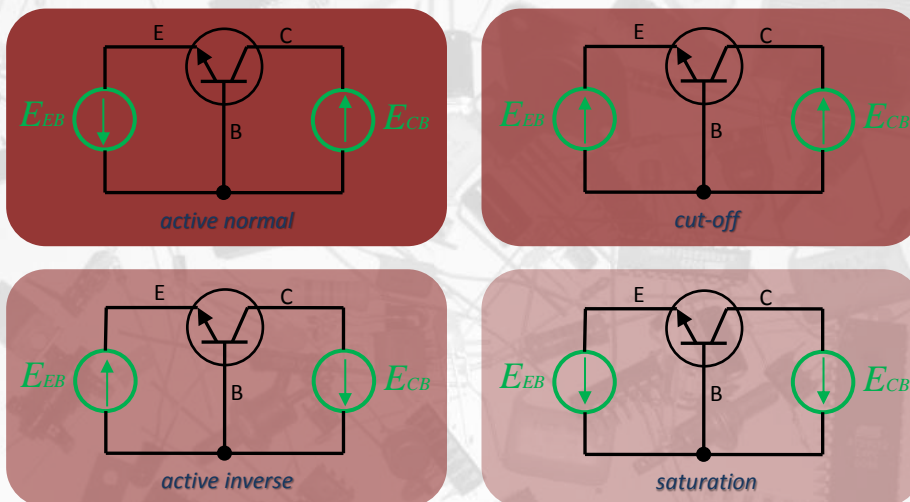
B - base

C - collector



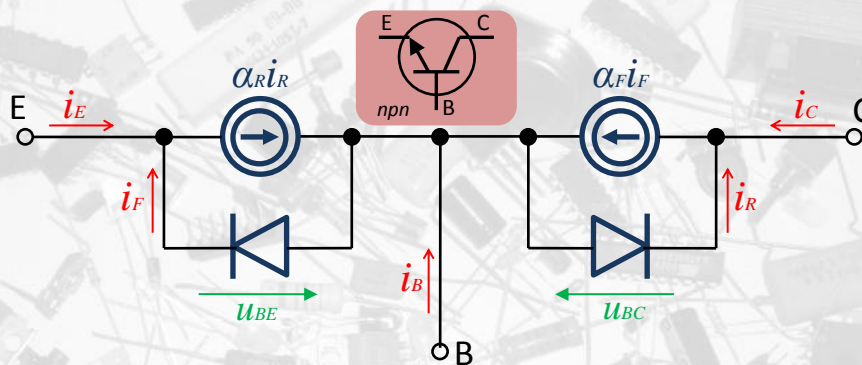
# BIPOLAR TRANSISTOR

## OPERATION STATES OF THE BIPOLAR TRANSISTOR



# BIPOLAR JUNCTION TRANSISTOR

## EBERS-MOLL MODEL



$$i_E = -I_{ES} \left( e^{\frac{u_{BE}}{n_E U_T}} - 1 \right) + \alpha_R I_{CS} \left( e^{\frac{u_{BC}}{n_C U_T}} - 1 \right)$$

$$i_C = \alpha_F I_{ES} \left( e^{\frac{u_{BE}}{n_E U_T}} - 1 \right) - I_{CS} \left( e^{\frac{u_{BC}}{n_C U_T}} - 1 \right)$$



# BIPOLAR JUNCTION TRANSISTOR

## EBERS-MOLL MODEL



$I_{ES}$  – reverse saturation emitter current at shorted collector junction

$$I_{ES} = \frac{I_{E0}}{1 - \alpha_F \alpha_R}$$

$I_{CS}$  – reverse saturation collector current at shorted emitter junction

$$I_{CS} = \frac{I_{C0}}{1 - \alpha_F \alpha_R}$$

$\eta_E, \eta_C$  – non-ideality factors of emitter and collector junctions

$\alpha_F$  – DC current gain of the transistor working in active normal configuration in OB mode

$$I_C = -\alpha_F I_E + I_{C0} \quad \alpha_F = \frac{I_C - I_{C0}}{I_E}$$

$\alpha_R$  – DC current gain of the transistor working in active reverse configuration in OB mode



# BIPOLAR JUNCTION TRANSISTOR

## EBERS-MOLL MODEL



$\alpha_F I_{ES} = \alpha_R I_{CS} \equiv I_S$  Onsager's identity

$I_S$  – transport saturation current

$$i_E = -\frac{I_S}{\alpha_F} \left( e^{\frac{u_{BE}}{n_E U_T}} - 1 \right) + I_S \left( e^{\frac{u_{BC}}{n_C U_T}} - 1 \right)$$

$$i_C = I_S \left( e^{\frac{u_{BE}}{n_E U_T}} - 1 \right) - \frac{I_S}{\alpha_R} \left( e^{\frac{u_{BC}}{n_C U_T}} - 1 \right)$$

E-M equations dependent only on three parameters



# BIPOLAR JUNCTION TRANSISTOR

## EBERS-MOLL MODEL

If we define as  $i_F$  the forward current of a emitter diode in active normal mode and the  $i_R$  as collector diode current in active inverse mode, then:

$$i_F = I_{ES} \left( e^{\frac{u_{BE}}{n_E U_T}} - 1 \right) = I_{ES} \left( e^{\frac{u_{BE}}{n_E U_T}} \right)$$

$$i_R = I_{CS} \left( e^{\frac{u_{BC}}{n_C U_T}} - 1 \right)$$

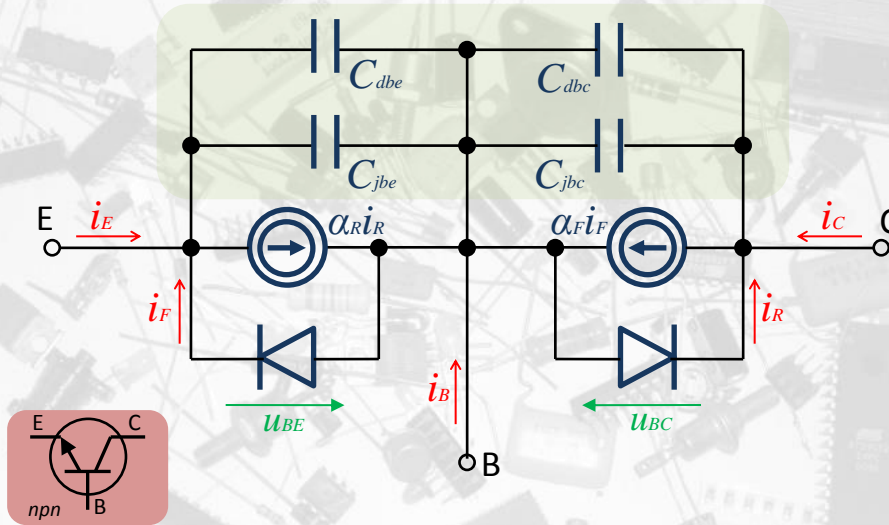
we get the E-M equations in the form of:

$$i_E = -i_F + \alpha_R i_R$$

$$i_C = \alpha_F i_F - i_R$$

# BIPOLAR JUNCTION TRANSISTOR

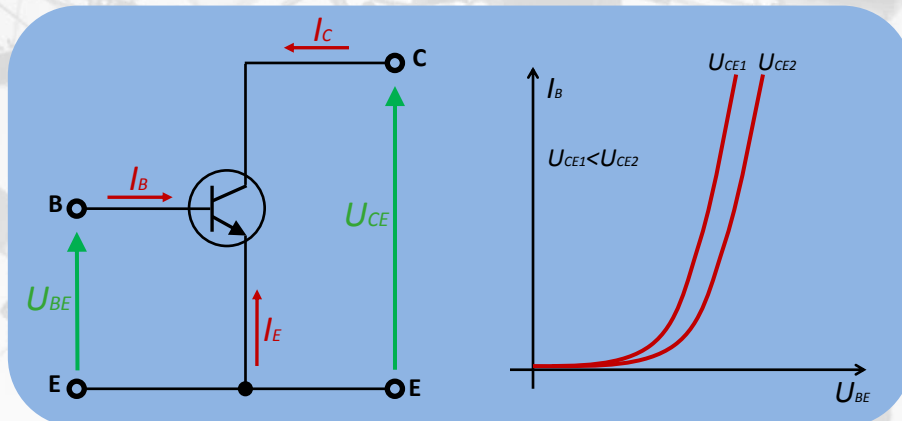
## EBERS-MOLL MODEL



## BIPOLAR JUNCTION TRANSISTOR OE MODE - CHARACTERISTICS

Input characteristics

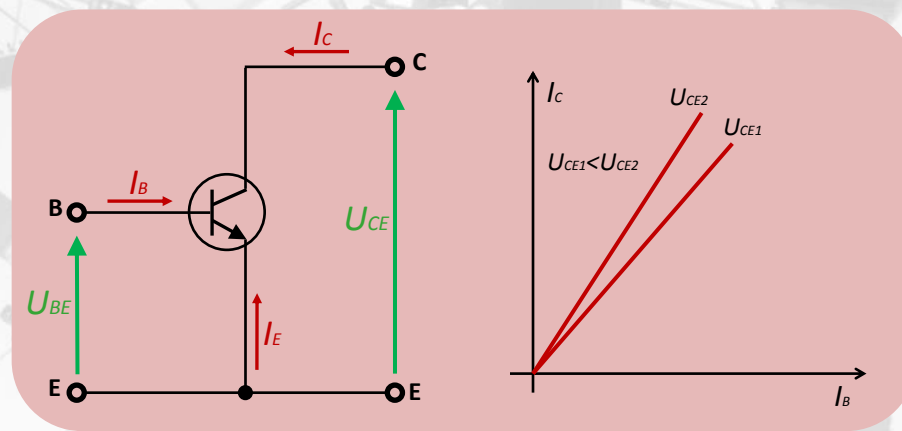
$$I_B = f(U_{BE}) \Big|_{U_{CE} = \text{const.}}$$



## BIPOLAR JUNCTION TRANSISTOR OE MODE - CHARACTERISTICS

Transitional characteristics

$$I_C = f(I_B) \Big|_{U_{CE} = \text{const.}}$$

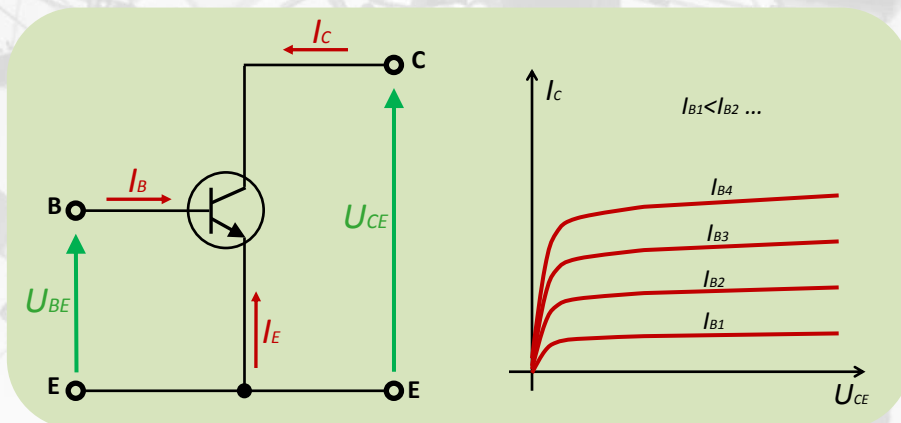


# BIPOLAR JUNCTION TRANSISTOR

## OE MODE - CHARACTERISTICS

Output characteristics

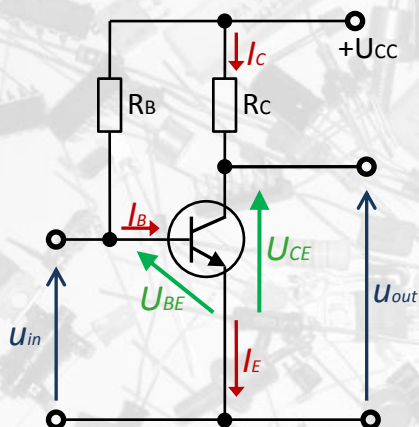
$$I_C = f(U_{CE}) \Big|_{I_B = \text{const.}}$$



# BIPOLAR JUNCTION TRANSISTOR

## OE MODE - ANALYSIS

Determination of operation point Q



$$U_{CC} = I_B \cdot R_B + U_{BE}$$

$$I_B = \frac{U_{CC} - U_{BE}}{R_B}$$

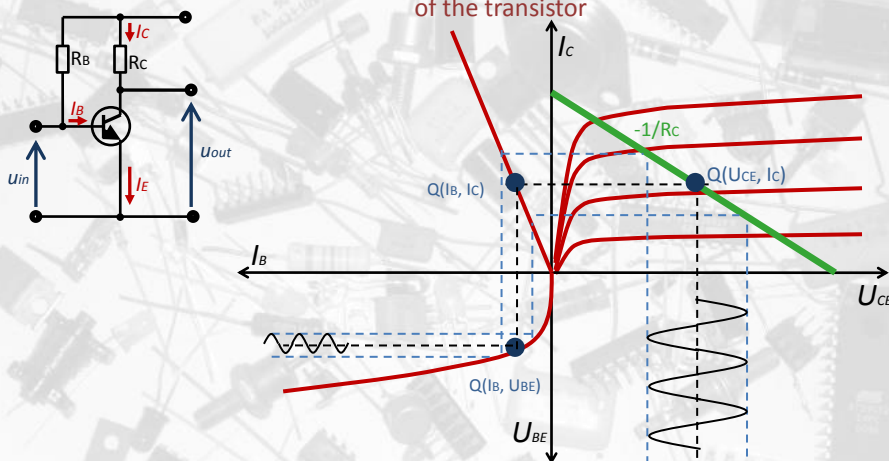
$$I_C = \beta \cdot I_B$$

$$U_{CC} = I_C \cdot R_C + U_{CE}$$

$$U_{CE} = U_{CC} - I_C \cdot R_C$$

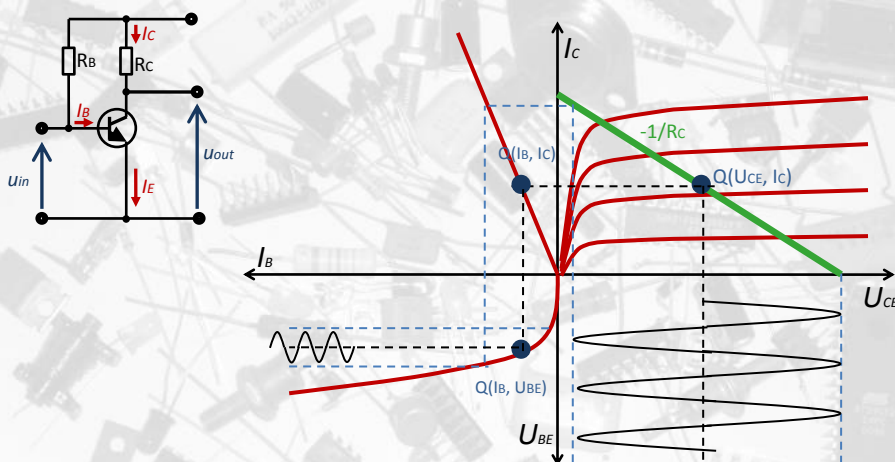
## BIPOLAR JUNCTION TRANSISTOR OE MODE - ANALYSIS

The impact of the choice of operation point on the amplifying properties of the transistor



## BIPOLAR JUNCTION TRANSISTOR OE MODE - ANALYSIS

The operating point for maximum dynamics of the output voltage



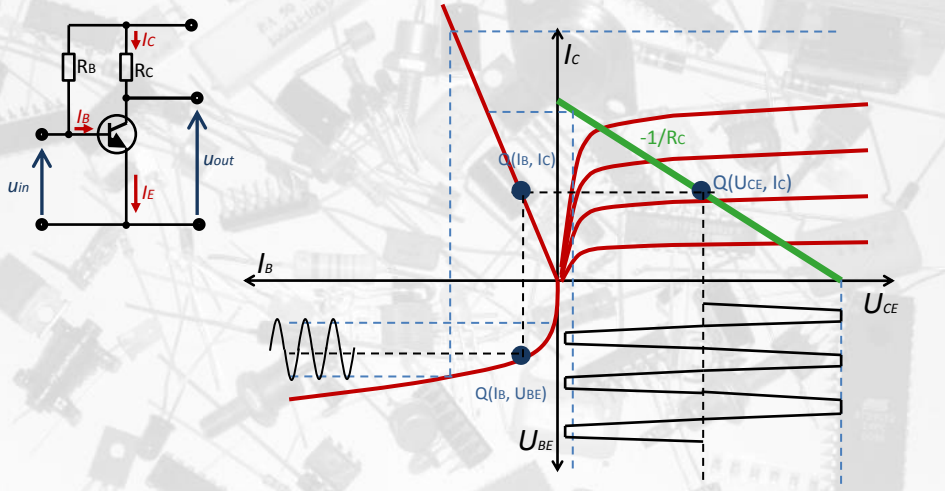


# BIPOLAR JUNCTION TRANSISTOR

## OE MODE - ANALYSIS



Output distortions due to too high input amplitude

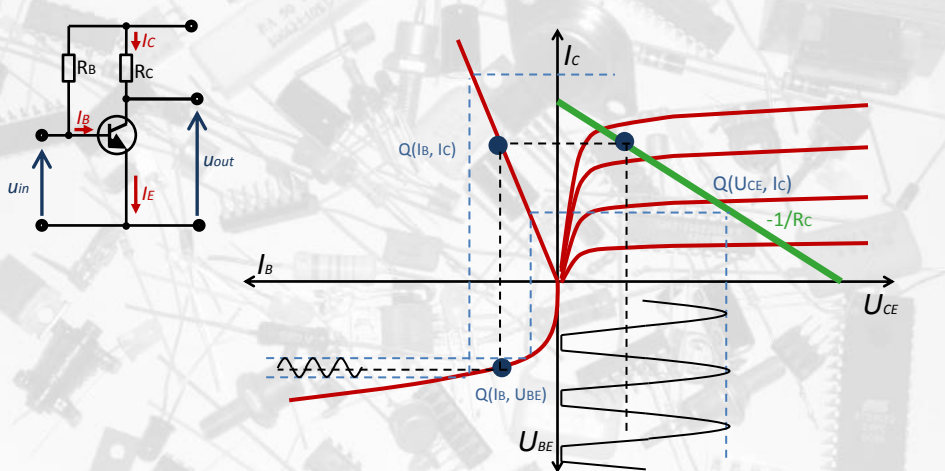


# BIPOLAR JUNCTION TRANSISTOR

## OE MODE - ANALYSIS



Operation point too close to the „saturation” area



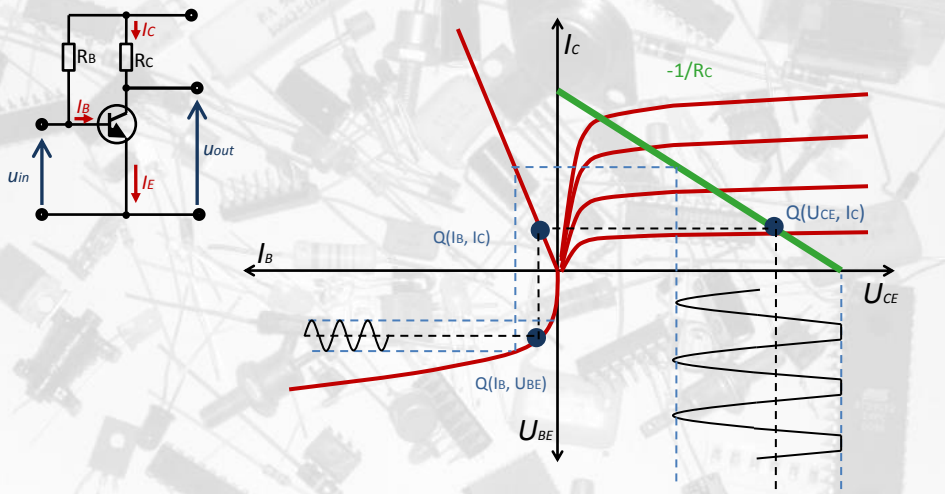


# BIPOLAR JUNCTION TRANSISTOR

## OE MODE - ANALYSIS



Operation point too close to the „cut-off” area

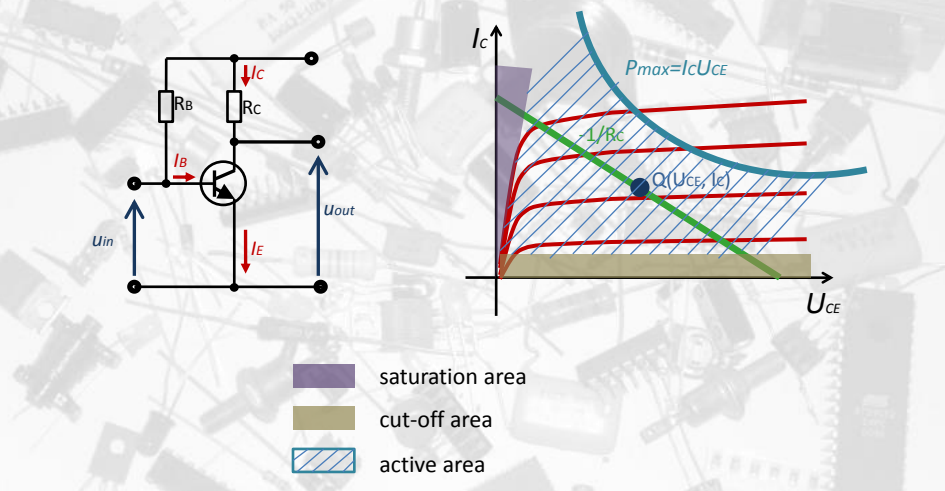


# BIPOLAR JUNCTION TRANSISTOR



## OE MODE - ANALYSIS



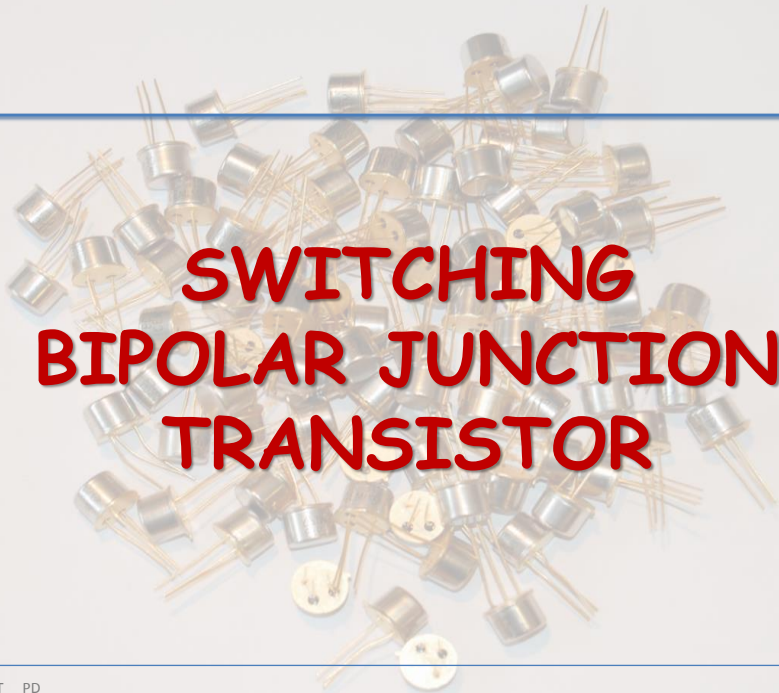
Operation area of the transistor at the output characteristics





- saturation area
- cut-off area
- active area

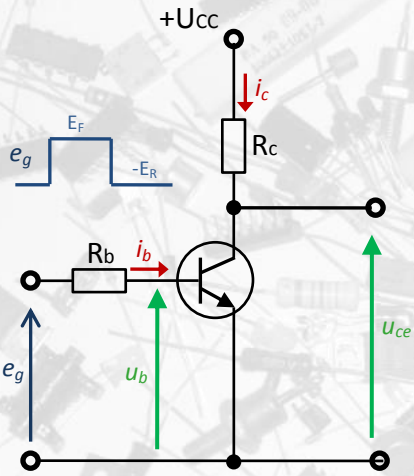
# SWITCHING BIPOLAR JUNCTION TRANSISTOR



EIT PD 29

## SWITCHING BIPOLAR JUNCTION TRANSISTOR



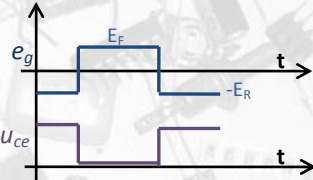
$$E_F = I_B \cdot R_b + U_{BEP}$$

$$I_B = \frac{E_F - U_{BEP}}{R_b}$$

$$I_C = \beta \cdot I_B$$


$$U_{CC} = I_C \cdot R_c + U_{CE}$$

$$U_{CE} = U_{CC} - I_C \cdot R_c$$




EIT PD 30

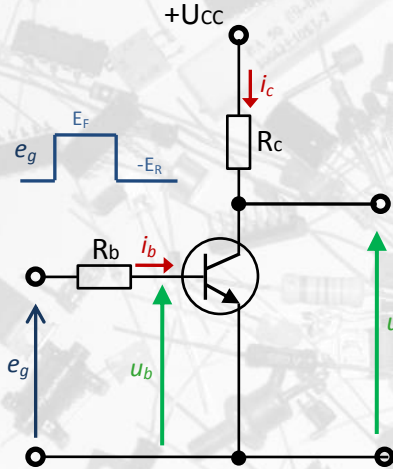
Electronic devices – Bipolar Junction Transistor

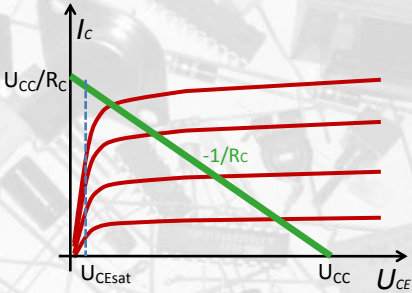


## SWITCHING BIPOLAR JUNCTION TRANSISTOR



---





$$I_{BF} = \frac{E_F - U_{BEP}}{R_b}$$


$$I_{CM} = \frac{U_{CC} - U_{CEsat}}{R_c} \approx \frac{U_{CC}}{R_c}$$

---


EIT PD

Electronic devices – Bipolar Junction Transistor

31



## SWITCHING BIPOLAR JUNCTION TRANSISTOR



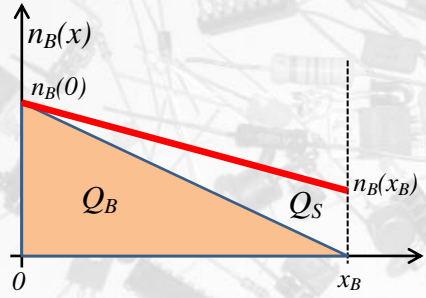
---

$$U_{CEsat} = U_T \ln \left[ \frac{1 + \frac{I_C}{I_B} \left( \frac{1 - \alpha_R}{\alpha_R} \right)}{1 - \frac{I_C}{I_B} \left( \frac{1 - \alpha_F}{\alpha_F} \right)} \right] = U_T \ln \left( \frac{1}{\alpha_R} \right)$$

$$I_C = \frac{Q_B}{\tau_i}$$

$$Q_B = \tau_{BF} I_B$$

$$\frac{\tau_i}{\tau_{BF}} = \frac{I_C}{I_B} = \beta$$



$$i_B(t) = \frac{Q_B}{\tau_{BF}} + \frac{dQ_B}{dt}$$

control equation of base charge in active normal mode

$$i_B(t) = \frac{Q_B}{\tau_{BF}} + \frac{Q_S}{\tau_S} + \frac{dQ_B}{dt} + \frac{dQ_S}{dt}$$

control equation of base charge in saturation mode

---

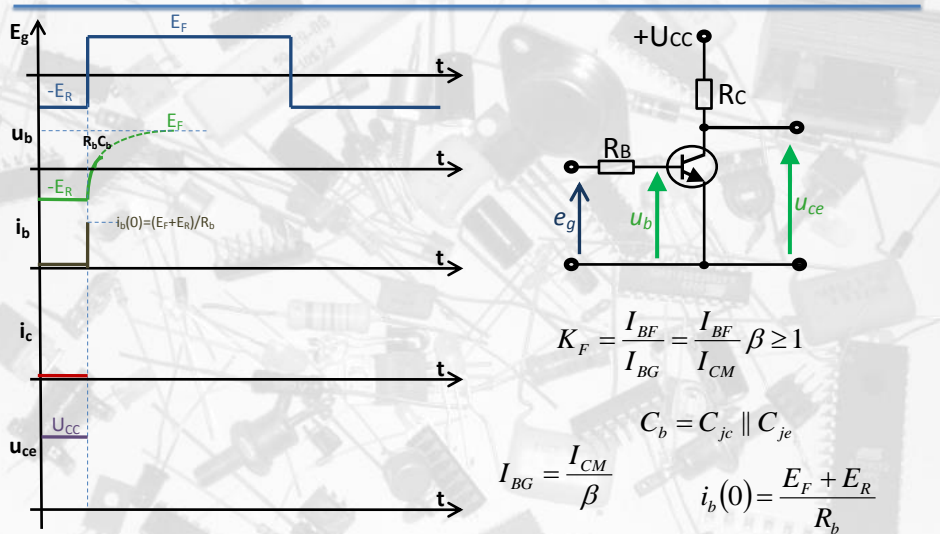
EIT PD

Electronic devices – Bipolar Junction Transistor

32

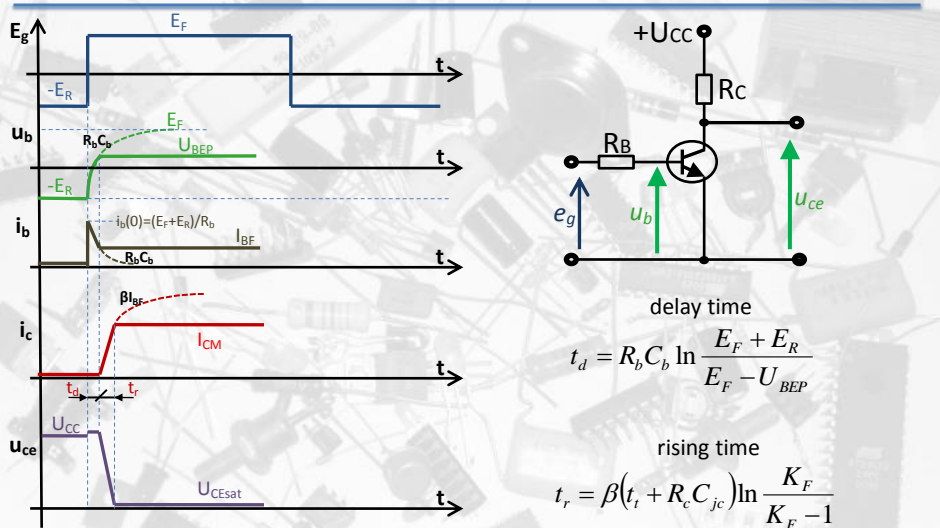


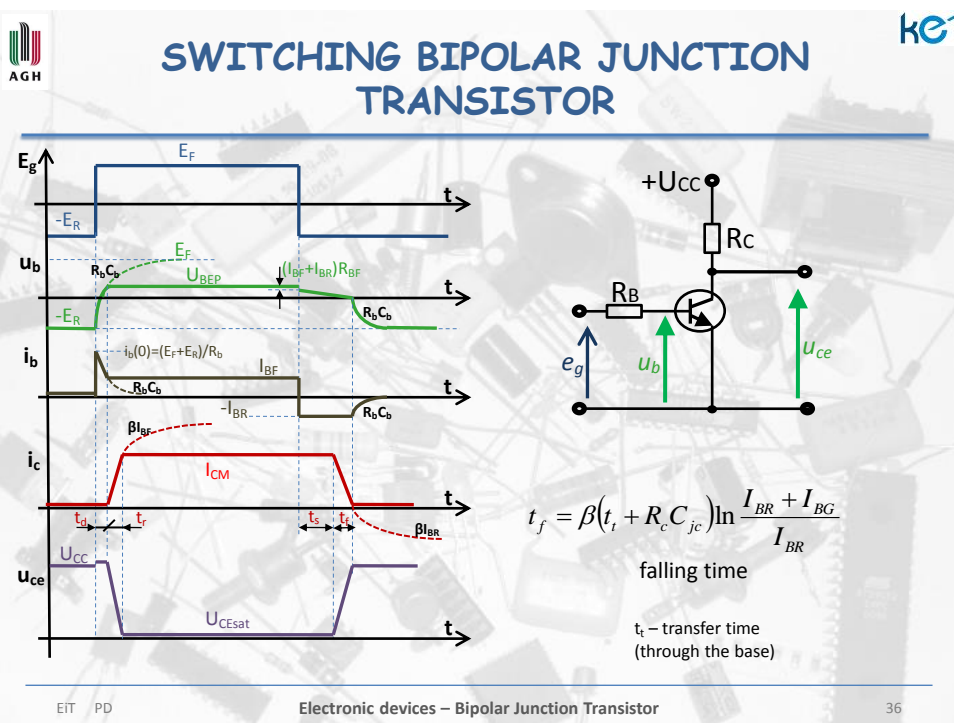
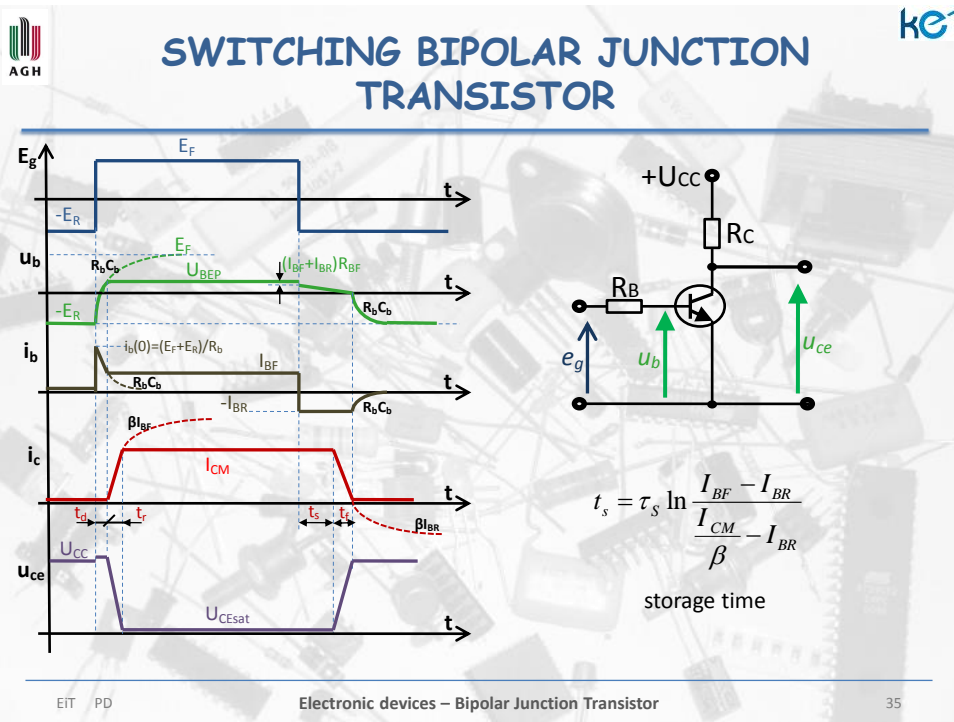
# SWITCHING BIPOLAR JUNCTION TRANSISTOR

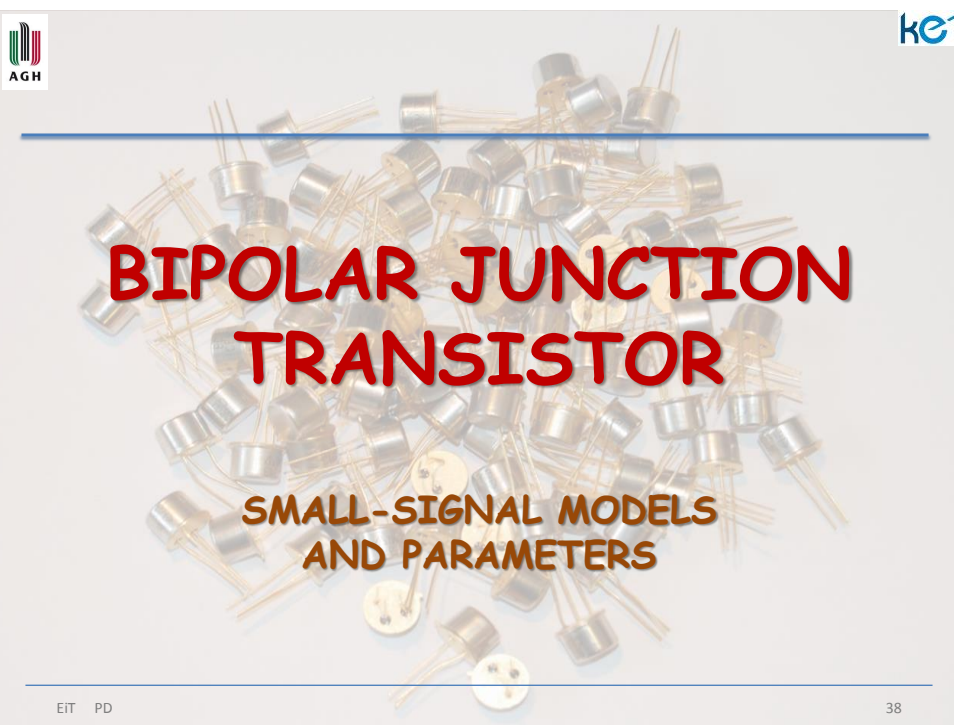
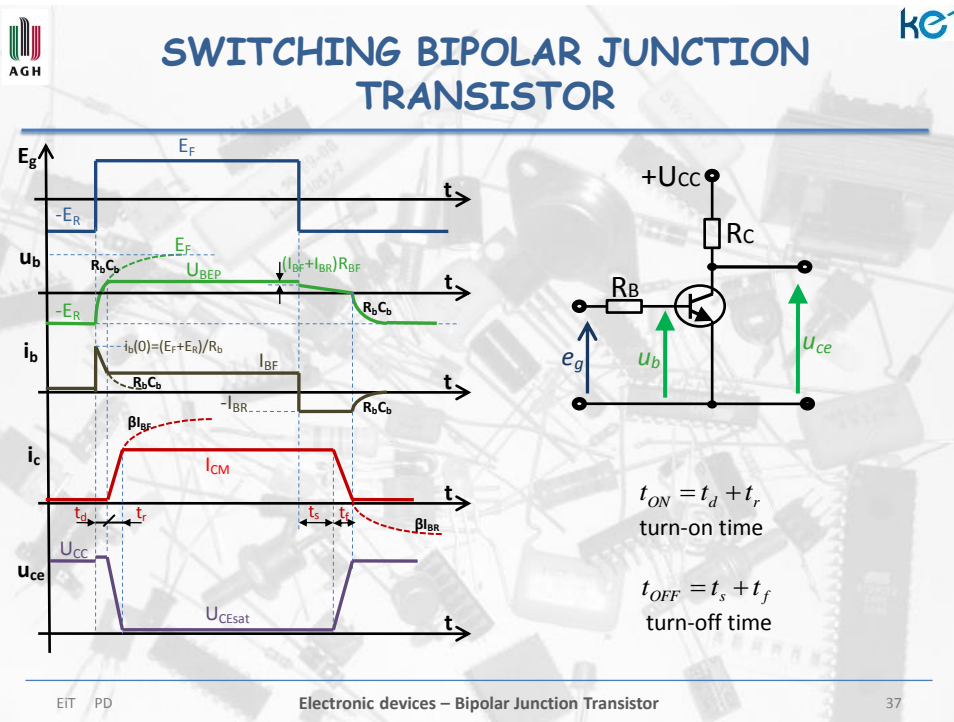


from: „Układy elektroniczne cz. II Układy analogowe nieliniowe i impulsowe”, J. Baranowski, G. Czajkowski

# SWITCHING BIPOLAR JUNCTION TRANSISTOR







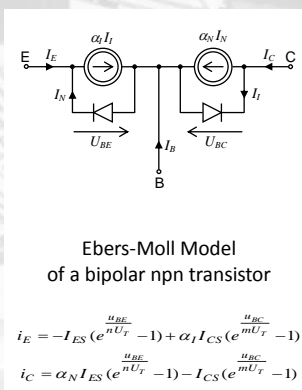
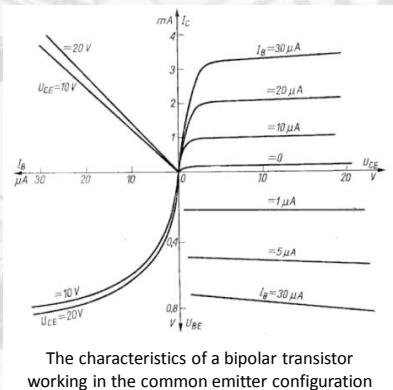
AGH **SMALL-SIGNAL MODEL - OBJECTIVES** ke

Transistor – a non linear device

Non linear characteristics

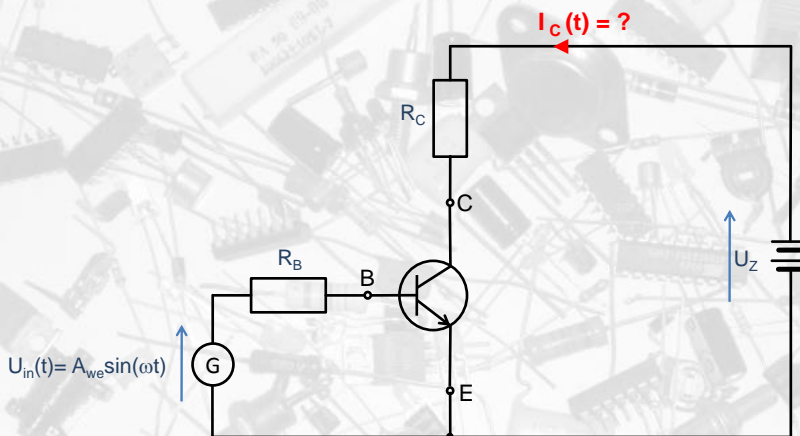


Non linear model



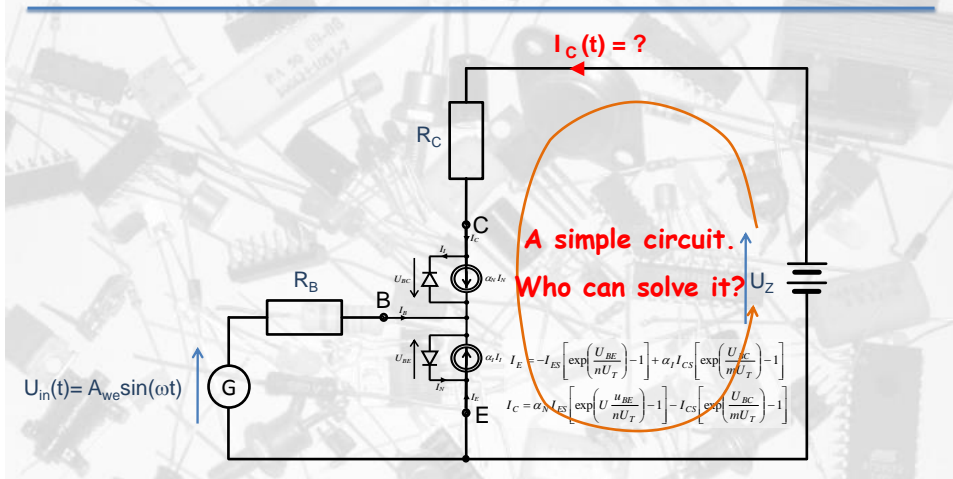
Picture from: W. Marciniak „Przyrządy półprzewodnikowe i układy scalone”, WNT 1979

AGH **SMALL-SIGNAL MODEL - OBJECTIVES** ke  
transistor in a circuit



Non linear Model (eg.: Ebers-Moll) is inconvenient for the analysis of the transistor in larger electronic circuits

AGH **SMALL-SIGNAL MODEL - OBJECTIVES** ke  
**transistor in a circuit**

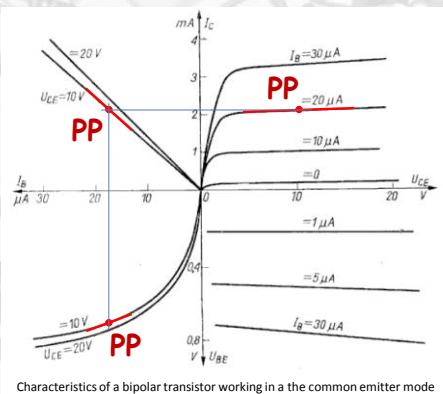


Non linear Model (eg.: Ebers-Moll) is inconvenient for the analysis of the transistor in larger electronic circuits

AGH **SMALL-SIGNAL MODEL - how to ...?** ke

Transistor – a non linear device → Linear model

How to do it ?




Around operation point PP


Linearization of characteristics

Model built of linear elements (with some minor limitations)

Picture from: W. Marciniak „Przyrządy półprzewodnikowe i układy scalone”, WNT 1979



## SMALL-SIGNAL MODEL



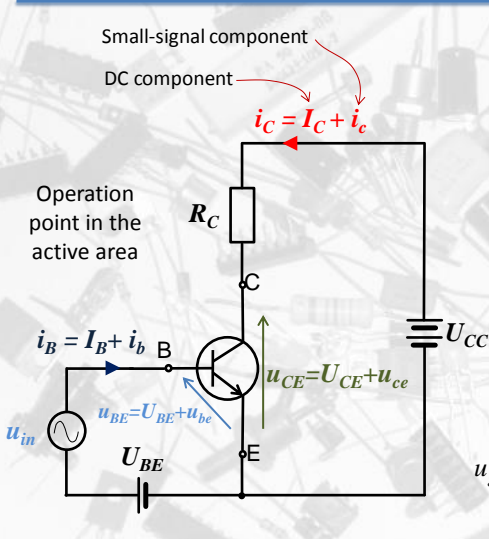
### transistor as an active four-terminal unit

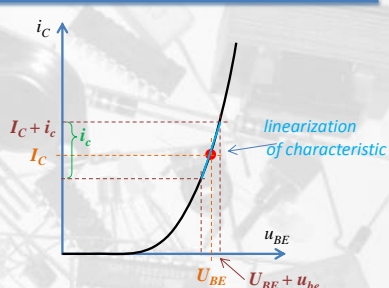
Small-signal component

DC component

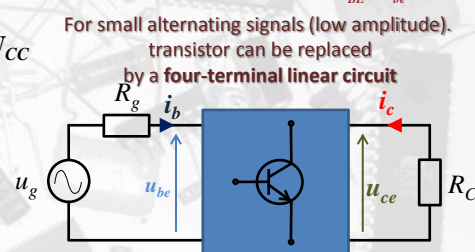
$i_c = I_C + i_c$

Operation point in the active area







For small alternating signals (low amplitude), transistor can be replaced by a **four-terminal linear circuit**



EIT PD
Electronic devices – Bipolar Junction Transistor
43

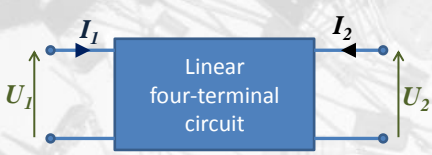


## LINEAR FOUR-TERMINAL MODELS



### (repetition - circuit theory)

In a general case:



Impedance equations:

$$U_1 = Z_{11} I_1 + Z_{12} I_2$$

$$U_2 = Z_{21} I_1 + Z_{22} I_2$$

Admittance equations:

$$I_1 = Y_{11} U_1 + Y_{12} U_2$$

$$I_2 = Y_{21} U_1 + Y_{22} U_2$$

Mixed equations (hybrid):

$$U_1 = H_{11} I_1 + H_{12} U_2$$

$$I_2 = H_{21} I_1 + H_{22} U_2$$

EIT PD
Electronic devices – Bipolar Junction Transistor
44

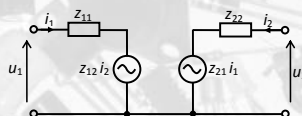
# FOUR-TERMINAL MODELS (small-signal models)

Small signals - we denote as: small letters small indexes

Impedance equations:

$$u_1 = z_{11} i_1 + z_{12} i_2$$

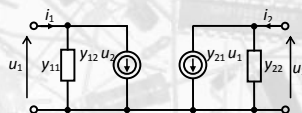
$$u_2 = z_{21} i_1 + z_{22} i_2$$



Admittance equations:

$$i_1 = y_{11} u_1 + y_{12} u_2$$

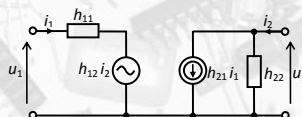
$$i_2 = y_{21} u_1 + y_{22} u_2$$



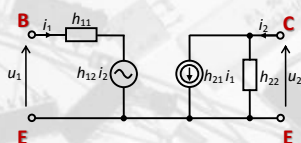
Hybrid equations:

$$u_1 = h_{11} i_1 + h_{12} u_2$$

$$i_2 = h_{21} i_1 + h_{22} u_2$$



# HYBRID MODEL parameters for OE



- Input impedance  
at shorted output (for small-signal voltage component at the output)

$$h_{11} = \left. \frac{u_1}{i_1} \right|_{u_2=0} = \left. \frac{\Delta u_{BE}}{\Delta i_B} \right|_{U_{CE}=const} = \left. \frac{u_{be}}{i_b} \right|_{u_{ce}=0} = h_{11e}$$

- Reverse voltage transfer function  
at open input (open small-signal current source at input)

$$h_{12} = \left. \frac{u_1}{u_2} \right|_{i_1=0} = \left. \frac{\Delta u_{BE}}{\Delta u_{CE}} \right|_{I_B=const} = \left. \frac{u_{be}}{u_{ce}} \right|_{i_b=0} = h_{12e}$$

- Current transmittance - current gain  
at shorted output (for small-signal voltage component at the output)

$$h_{21} = \left. \frac{i_2}{i_1} \right|_{u_2=0} = \left. \frac{\Delta i_C}{\Delta i_B} \right|_{U_{CE}=const} = \left. \frac{i_c}{i_b} \right|_{u_{ce}=0} = h_{21e}$$

- Output admittance  
at open input (open small-signal current source at input)

$$h_{22} = \left. \frac{i_2}{u_2} \right|_{i_1=0} = \left. \frac{\Delta i_C}{\Delta u_{CE}} \right|_{I_B=const} = \left. \frac{i_c}{u_{ce}} \right|_{i_b=0} = h_{22e}$$

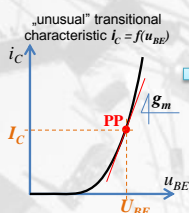
## HYBRID MODEL

### parameters for different configurations

	OE	OB	OC
$h_{11}$	$h_{11e}$	$h_{11b} = \frac{h_{11e}}{1 + h_{21e}}$	$h_{11c} = h_{11e}$
$h_{12}$	$h_{12e}$	$h_{12b} = \frac{h_{11e}h_{22e}}{1 + h_{21e}} - h_{12e}$	$h_{12c} = 1 - h_{12e}$
$h_{21}$	$h_{21e}$	$h_{21b} = \frac{h_{21e}}{1 + h_{21e}}$	$h_{21c} = 1 + h_{21e}$
$h_{22}$	$h_{22e}$	$h_{22b} = \frac{h_{22e}}{1 + h_{21e}}$	$h_{22c} = h_{22e}$

## SMALL-SIGNAL MODEL of BJT

### Representation of physical phenomena occurring in the transistor - equivalent circuit



- Transconductance – the impact of input to output

$$g_m \equiv \left. \frac{\partial i_C}{\partial U_{BE}} \right|_{U_{BE}, U_{CE} = \text{const}}$$

translational characteristic

- Feedback transconductance – the impact of the output voltage to the input

$$g_f \equiv \left. \frac{\partial i_B}{\partial U_{CE}} \right|_{U_{BE}, U_{CE} = \text{const}}$$

feedback characteristic

- Input conductance – input characteristic (transistor seen „at the input side“)

$$g_z \equiv \left. \frac{\partial i_B}{\partial U_{BE}} \right|_{U_{BE}, U_{CE} = \text{const}}$$

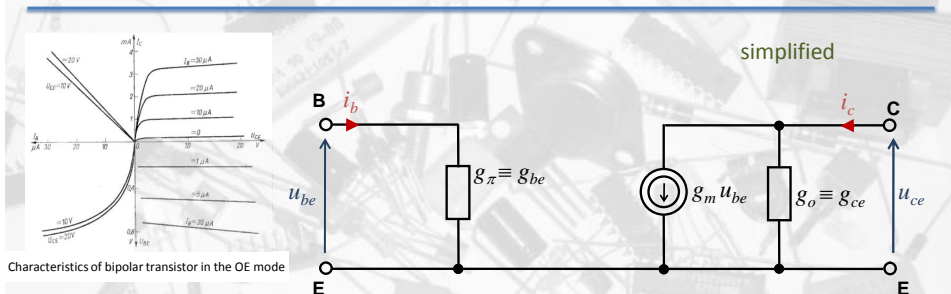
input characteristic

- Output conductance – output characteristic (transistor seen „at the output side“)

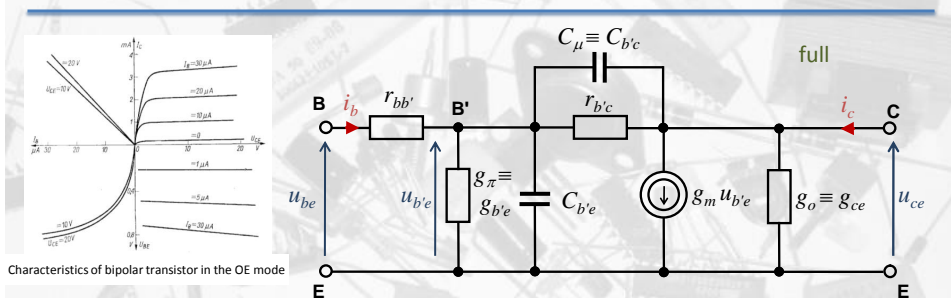
$$g_o \equiv \left. \frac{\partial i_C}{\partial U_{CE}} \right|_{U_{BE}, U_{CE} = \text{const}}$$

output characteristic



HYBRID- $\pi$  MODEL for OE MODE

- at the output side: current source controlled from the input:  $g_m u_{be}$
- at the input side: input conductance:  $g_\pi \equiv g_{be}$
- at the output side : output conductance:  $g_o \equiv g_{ce}$

HYBRID- $\pi$  MODEL for OE MODE

- at the output side: current source controlled from the input:  $g_m u_{be}$
- at the input side: input conductance:  $g_\pi \equiv g_{be}$
- at the output side : output conductance:  $g_o \equiv g_{ce}$
- at the input side : base resistance:  $r_{bb'}$
- from the input to the output a direct: resistive feedback base-collector:  $r_{b'c}$
- capacitance of emitter junction  $C_{b'e}$  and capacitance of collector junction  $C_{b'c}$

## HYBRID- $\pi$ MODEL for OE MODE

### estimation of parameters (1)

Transconductance  $g_m$

- From a definition:  $g_m = \frac{\partial I_C}{\partial U_{BE}}$  the slope of „unusual“ transitional characteristic  $I_C = f(U_{BE})$
- From a operation point: differentiating emitter diode current from Ebers-Moll model:

$$g_m = \frac{\partial(\alpha I_E)}{\partial U_{BE}} = \alpha \frac{I_E}{n_E U_T}$$

simplified relationship

and taking into account collector current ( $I_C = \alpha I_E$ ):

$$I_E = I_{ES} (e^{\frac{U_{BE}}{n_E U_T}} - 1)$$

$$g_m = \frac{I_C}{n_E U_T}$$

In practice  $n_E = 1$

$$g_m = \frac{I_C}{U_T}$$

$\alpha$  – coefficient of current gain for OB mode  
 $n_E$  – coefficient of non ideality of emitter junction  
 $U_T$  – electrothermal potential  
 $I_C$  – constant collector current

## HYBRID- $\pi$ MODEL for OE MODE

### estimation of parameters (2)

Input conductance  $g_{b'e}$

- From a definition:  $g_{b'e} = \frac{\partial I_B}{\partial U_{BE}}$  slope of the input characteristic  $I_B = f(U_{BE})$

- From the operation point: for OE mode, it is:

$$\begin{aligned} I_E &= I_C + I_B \\ I_B &= I_E - I_C, \quad I_C = \alpha I_E \\ I_B &= I_E (1 - \alpha) \end{aligned}$$

therefore from Ebers-Moll model, base current for OE is:  $I_B \approx (1 - \alpha) I_{ES} \exp\left(\frac{U_{BE}}{n_E U_T}\right)$

Taking into account the definition:  $g_{b'e} = \frac{1}{n_E U_T} (1 - \alpha) I_{ES} \exp\left(\frac{U_{BE}}{n_E U_T}\right)$

$$g_{b'e} = \frac{I_B}{n_E U_T}$$

and:  $I_B = \frac{I_C}{\beta_0}$

we have:  $g_{b'e} = \frac{I_C}{\beta_0 n_E U_T} = \frac{g_m}{\beta_0}$

$\alpha$  – coefficient of current gain for OB  
 $\beta_0$  – coefficient of current gain for OE  
 $n_E$  – coefficient of non ideality of emitter junction  
 $U_T$  – electrothermal potential  
 $I_C$  – constant collector current

## HYBRID- $\pi$ MODEL for OE MODE

### estimation of parameters (3)

Distributed resistance of the base  $r_{bb'}$

- From the comparison of hybrid- $\pi$  and hybrid models :

$$r_{bb'} = h_{11e} - r_{b'e}$$

Output conductance  $g_{ce}$

- From a definition:  $g_{ce} = \frac{\partial I_C}{\partial U_{CE}}$

$$g_{ce} \equiv h_{22e}$$

- Taking into account Early effect:  $I_C = \beta_0 I_B \left( 1 + \frac{U_{CE}}{U_A} \right)$

$U_A$  – Early voltage

- And after differentiation:  $g_{ce} = \beta_0 I_B \frac{1}{U_A} = \frac{I_C}{U_A + U_{CE}}$

Resistive feedback  $r_{b'c}$

- From a definition:  $r_{b'c} = \frac{\partial U_{CB}}{\partial I_B}$

- but  $U_{CB} \gg U_{BE'}$ , then:  $r_{b'c} \approx \frac{\partial U_{CE}}{\partial I_C} = \frac{\beta_0}{g_{ce}} = \beta_0 \frac{U_A + U_{CE}}{I_C}$  but if:  $U_A \gg U_{CE}$ , then:

$$r_{b'c} \approx \beta_0 \frac{U_A}{g_m U_T}$$

## HYBRID- $\pi$ MODEL for OE MODE

### estimation of parameters (4)

Input capacitance  $C_\pi$  – emitter junction  $C_{b'e}$

$$C_\pi \equiv C_{b'e} = C_{de} + C_{je}$$



junction capacitance

diffusion capacitance

$$C_{b'e} = C_{de} = \tau_F \frac{I_E}{U_T} = \tau_F g_{b'e} \frac{1}{1-\alpha}$$

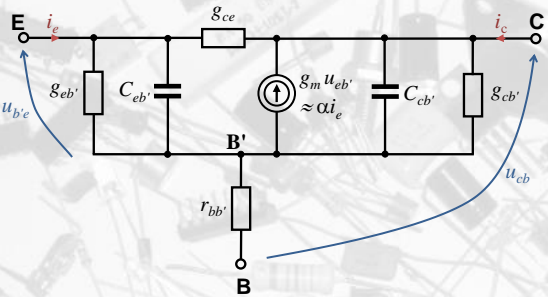
Coupled capacitance  $C_\mu$  – collector junction  $C_{b'c}$

Junction capacitance of reverse biased base-collector junction

## HYBRID- $\pi$ MODEL for OB MODE

---



$$\alpha \equiv \left. \frac{\partial i_c}{\partial i_e} \right|_{U_{BC}=\text{const}}$$

$$g_{b'e} \equiv \left. \frac{\partial i_E}{\partial u_{b'e}} \right|_{U_{BE}, U_{BC}=\text{const}} \approx \frac{I_E}{U_T}$$



$$g_{b'e} = \frac{g_{cb'}}{\beta_0 + 1} \quad g_{b'e} \approx g_m$$

---

EIT PD

Electronic devices – Bipolar Junction Transistor

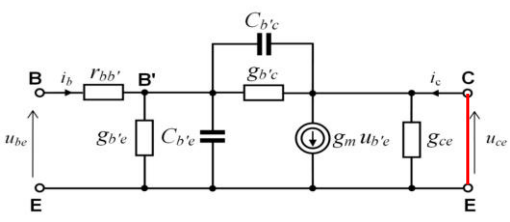
55

## FREQUENCY LIMITS

---

When, for which frequency does transistor stop to fulfil its basic function, that is amplification of signals?



applicability :  $\tau_F \ll T$

small-signal current gain  $\beta$  for shorted output:

$$\beta(j\omega) \equiv \left. \frac{i_c}{i_b} \right|_{u_{ce}=0}$$

$$\beta(j\omega) = \frac{g_m u_{b'e}(j\omega)}{i_b(j\omega)}$$

$$\beta(j\omega) = \frac{g_m}{1 + j\omega \left( \frac{C_{b'e} + C_{b'c}}{g_{b'e}} \right)}$$

---

EIT PD

Electronic devices – Bipolar Junction Transistor

56

## FREQUENCY LIMITS

- Cut-off frequency  $f_\beta$

- when  $\beta(f)$  is reduced by 3dB:

$$\beta(j\omega) = \frac{g_m}{1 + j\omega \left( \frac{C_{b'e} + C_{b'c}}{g_{b'e}} \right)}$$

Denoting:  $G = \frac{g_m}{g_{b'e}}$ ,  $X = \frac{C_{b'e} + C_{b'c}}{g_{b'e}}$

$$\beta(j\omega) = \frac{G}{1 + j\omega X} = \frac{G}{1 + \omega^2 X^2} + j \frac{\omega GX}{1 + \omega^2 X^2}$$

$$|\beta(f_\beta)| \equiv \frac{\beta_0}{\sqrt{2}}$$

$$|\beta(j\omega)| = \sqrt{\left( \frac{G}{1 + \omega^2 X^2} \right)^2 + \left( \frac{\omega GX}{1 + \omega^2 X^2} \right)^2} \quad |\beta(f_\beta)| \equiv \frac{\beta_0}{\sqrt{2}}$$

$$\sqrt{\left( \frac{G}{1 + \omega_\beta^2 X^2} \right)^2 + \left( \frac{\omega_\beta GX}{1 + \omega_\beta^2 X^2} \right)^2} = \frac{\beta_0}{\sqrt{2}}$$

$$\left( \frac{G^2 + \omega_\beta^2 X^2 G^2}{1 + \omega_\beta^2 X^2} \right) = \frac{\beta_0^2}{2}$$

$$1 + \omega_\beta^2 X^2 = 2 \frac{G^2}{\beta_0^2} \quad \omega_\beta = \frac{1}{X} \quad \omega_\beta = \frac{g_{b'e}}{C_{b'e} + C_{b'c}}$$

$$f_\beta = \frac{g_{b'e}}{2\pi(C_{b'e} + C_{b'c})}$$

$$g_{b'e} = \frac{g_m}{\beta_0}$$

## FREQUENCY LIMITS

- Cut-off frequency  $f_\alpha$

- when  $\alpha(f)$  is reduced by 3dB:

$$|\alpha(f_\alpha)| \equiv \frac{\alpha_0}{\sqrt{2}}$$

Following the analogous procedure as for  $f_\beta$ :

$$f_\alpha = \frac{g_{eb'}}{2\pi C_{eb'}}$$

## FREQUENCY LIMITS

- Threshold frequency  $f_T$ 
  - when magnitude of  $\beta(f)=1$

$$\beta(j\omega) = \frac{g_m}{1 + j\omega \left( \frac{C_{b'e} + C_{b'c}}{g_{b'e}} \right)}$$

$$\beta(f) = \frac{\beta_0}{1 + j \frac{f}{f_\beta}} \rightarrow \frac{\beta(f)}{\beta_0} = \frac{1}{1 + j \frac{f}{f_\beta}} \approx -j \frac{f_\beta}{f} \quad \text{when: } f > f_\beta$$

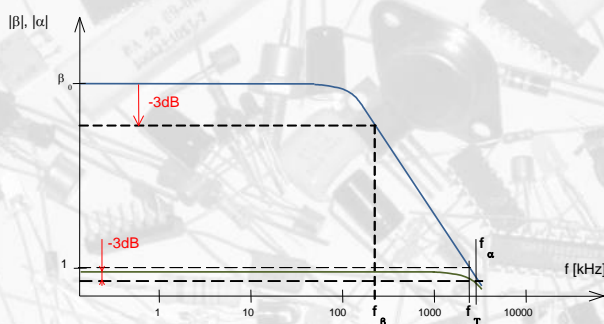
$$f_\beta = \frac{g_{b'e}}{2\pi(C_{b'e} + C_{b'c})}$$

for:  $f = f_T$  it is:  $|\beta(f)|=1$

$$\frac{1}{\beta_0} = \frac{f_\beta}{f_T}$$

$$f_T = \beta_0 f_\beta$$

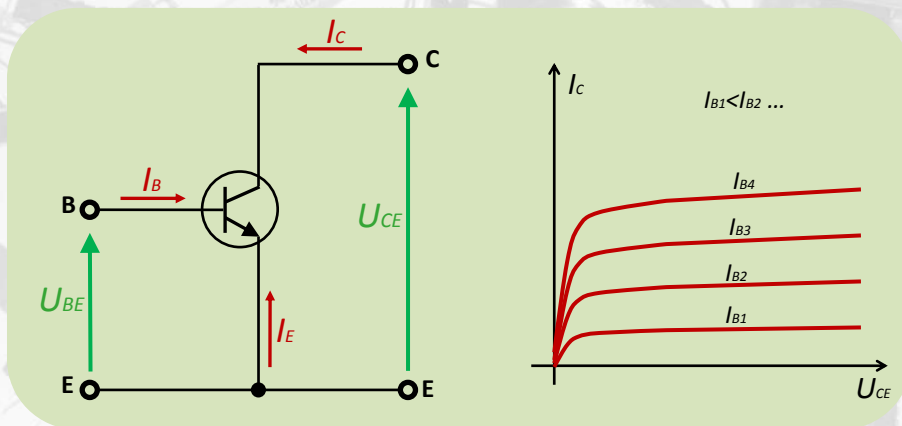
## FREQUENCY LIMITS



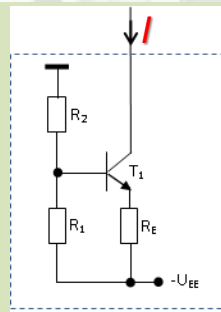
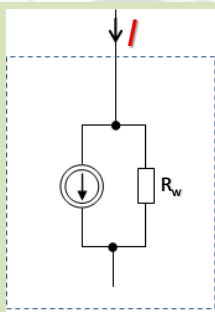
Bipolar transistor operating in a common base mode has  $\beta$ -times higher cut-off frequency

$$f_\alpha \approx \beta_0 f_\beta$$

# CURRENT SOURCES



# CURRENT SOURCES



$$R_w = R_E I I (r_{be} + R_B) + r_{ce} \left( 1 + g_m \frac{R_E + r_{be}}{R_E + r_{be} + R_B} \right)$$

$$R_B = R_1 || R_2$$

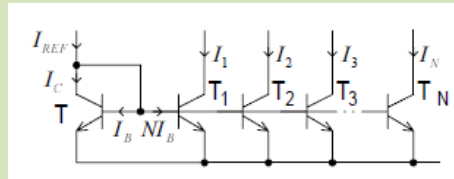
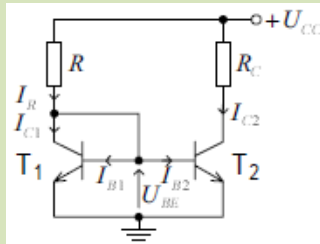
$$U_{R1} = U_{RE} + U_{BE}$$

$$U_{EE} \frac{R_1}{R_1 + R_2} = I R_E + U_{BE}$$

$$I = \frac{U_{EE} \frac{R_1}{R_1 + R_2} - U_{BE}}{R_E}$$

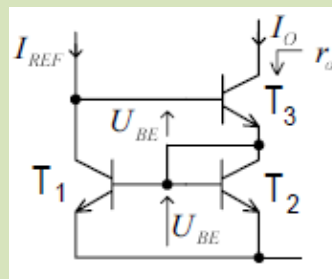
## CURRENT SOURCES

### Current mirrors



## CURRENT SOURCES

### Wilson's current mirror



$$r_o \approx \beta_0 \frac{U_A}{2I_O}$$